

Form PTO-1449 (modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT	Attorney Docket No. 2207/793203	Serial No. of Parent Application 10/060,264
	Applicants Amit A. MERCHANT et al.	
	Filing Date of Parent Application February 1, 2002	Group Art Unit of Parent Application 2154

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>[Signature]</i>	5,185,871	February 9, 1993	Frey et al.	712	205	December 26, 1989
<i>[Signature]</i>	5,197,132	March 23, 1993	Steely, Jr. et al.	395	375	June 29, 1990
<i>[Signature]</i>	5,341,482	August 23, 1994	Cutler et al.	712	244	December 22, 1992
<i>[Signature]</i>	5,519,841	May 21, 1996	Sager et al.	711	202	November 12, 1992
<i>[Signature]</i>	5,828,868	October 27, 1998	Sager et al.	395	556	November 13, 1996
<i>[Signature]</i>	5,835,745	November 10, 1998	Sager et al.	395	391	March 7, 1996
<i>[Signature]</i>	5,966,544	October 12, 1999	Sager	712	32	November 13, 1996
<i>[Signature]</i>	6,088,790	July 11, 2000	Grochowski	712	218	December 19, 1997
<i>[Signature]</i>	6,094,717	July 25, 2000	Merchant et al.	712	32	July 31, 1998
<i>[Signature]</i>	6,163,838	December 19, 2000	Merchant et al.	712	219	June 30, 1998
<i>[Signature]</i>	6,542,921	April 1, 2001	Sager	709	108	July 8, 1999
<i>[Signature]</i>	6,212,544	April 3, 2001	Borkenhagen et al.	709	103	October 23, 1997
<i>[Signature]</i>	6,507,862	January 14, 2003	Joy et al.	709	107	May 11, 1999
<i>[Signature]</i>	6,535,905	March 18, 2003	Kalafatis et al.	709	108	March 18, 2003

**A copy is not enclosed since the present application was filed after June 30, 2003. (See OG Dated: August 5, 2003)

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
<i>[Signature]</i>	Wallace et al., Threaded Multiple Path Execution, June 1998, pp.1-12.
<i>[Signature]</i>	Roos et al., Execution Replay of Parallel Programs, 1993, pp. 1-6.
<i>[Signature]</i>	Marcuello et al., Data Speculative Multithreaded Architecture, 1998, pp. 321-324.
<i>[Signature]</i>	Gulati et al., Performance Study of a Multithreaded Superscalar Microprocessor, 1996, pp. 291-301.
<i>[Signature]</i>	Akkary et al., A Dynamic Multithreading Processor, 1998, pp. 226-236.
<i>[Signature]</i>	Tsai et al., Performance Study of a Concurrent Multithreaded Processor, 1998, pp. 24-35.
<i>[Signature]</i>	Lohikainen et al., A Fine-Grain Multithreading Superscalar Architecture, 1996, pp. 163-168.

EXAMINER	DATE CONSIDERED <i>03/25/05</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	